






12/27/00

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

<p>  </p> <p> O.I.P.E. </p> <p> SCANNED  Q.A.  </p>	<p>PATENT DATE</p>
--	--------------------

APPLICATION NO. 09/748256	CONT/PRIOR D	CLASS 438 257	SUBCLASS 301	ART UNIT 28.12	EXAMINER R. L. Nadeau
------------------------------	-----------------	--------------------------------	-----------------	-------------------	--------------------------

APPLICANTS

Howard Chen
Louis Hsu
Li-Kong Wang

TITLE

Method for fabricating complementary metal oxide semiconductor (CMOS) devices on a mixed bulk and silicon-on-insulator (SOI) substrate

PTO-2040
12/99

CPA

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. 	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	_____ (Legal Instruments Examiner) (Date)				

WARNING:

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-436A
(Rev. 6/99)

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM
(Attached in pocket on right inside flap)

BEST AVAILABLE COPY